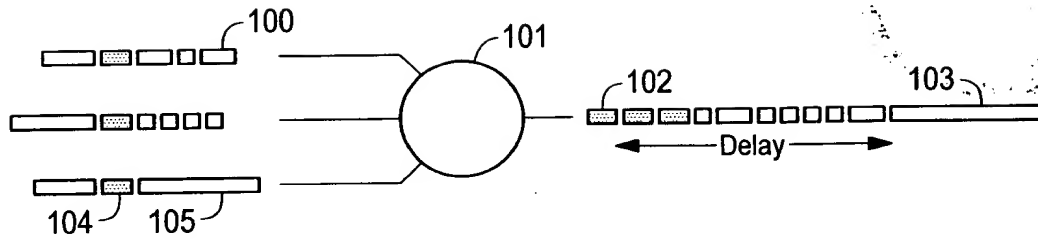
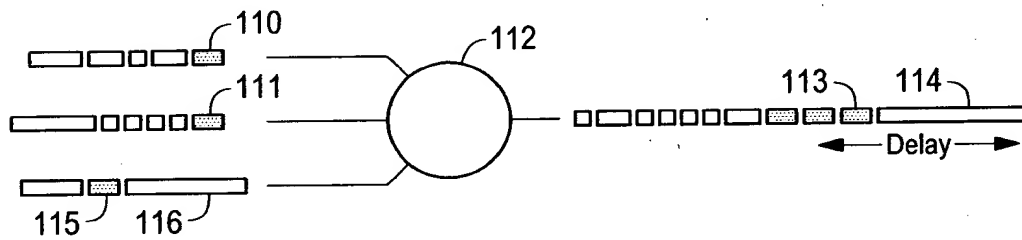


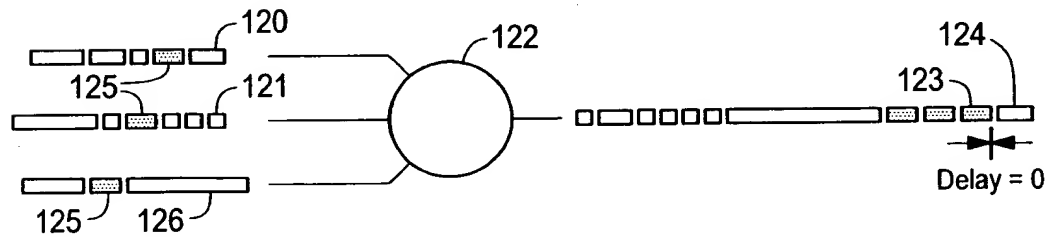
FIG. 1



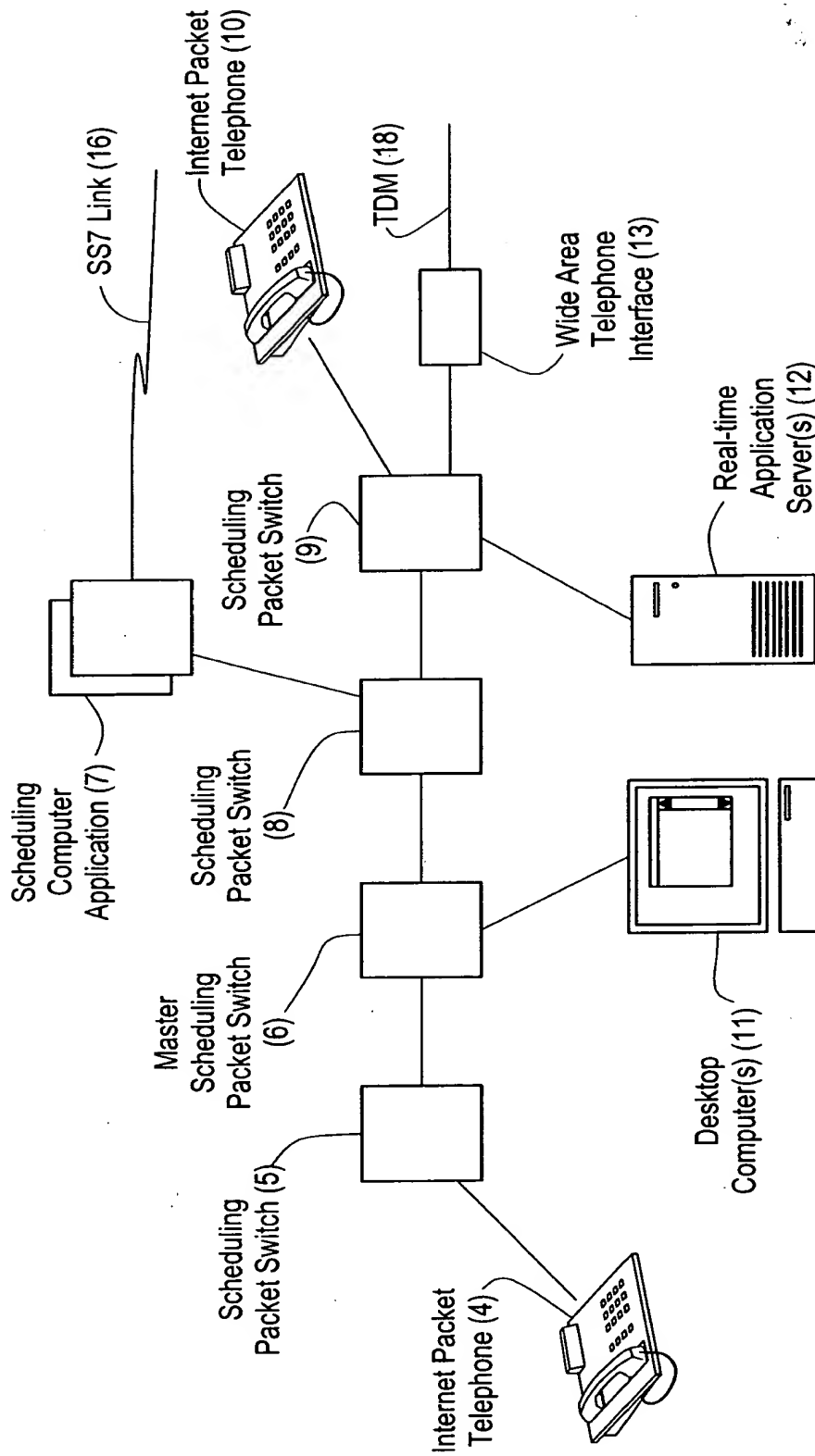
STANDARD
FIG. 2A



PRIORITY-BASED
FIG. 2B

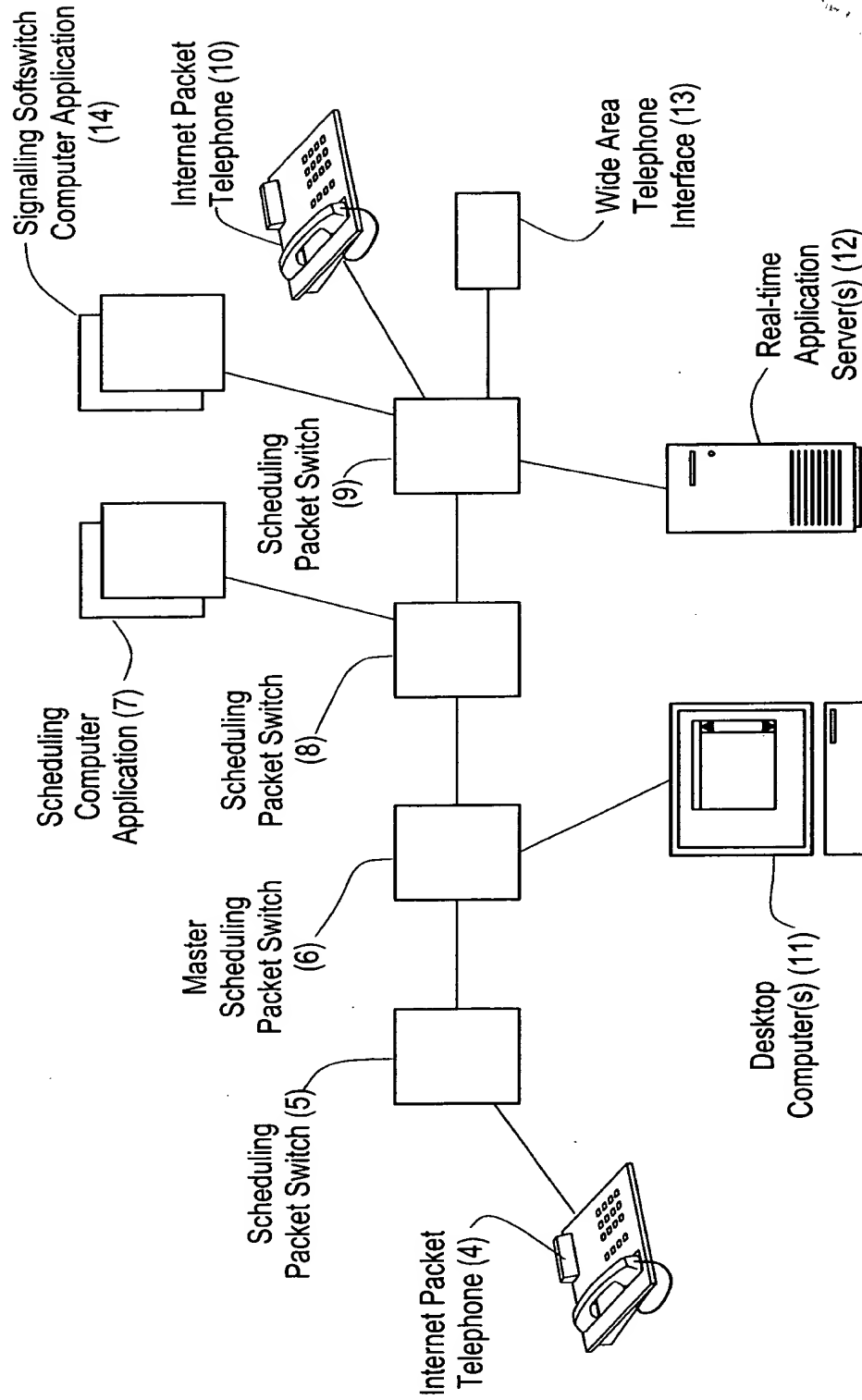


SCHEDULE-BASED
FIG. 2C



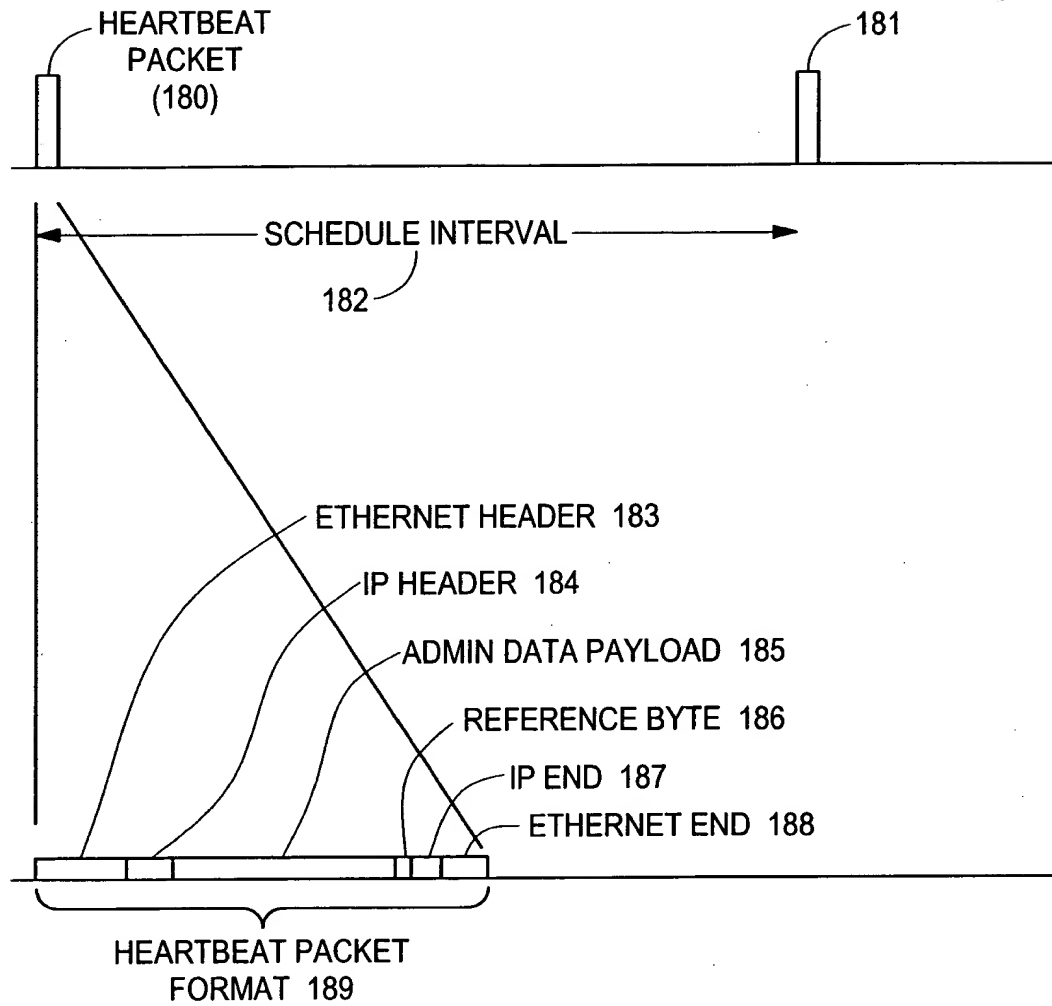
Network of Scheduled Switches

FIG. 3



Network of Scheduled Switches with Signalling Softswitch

FIG. 4

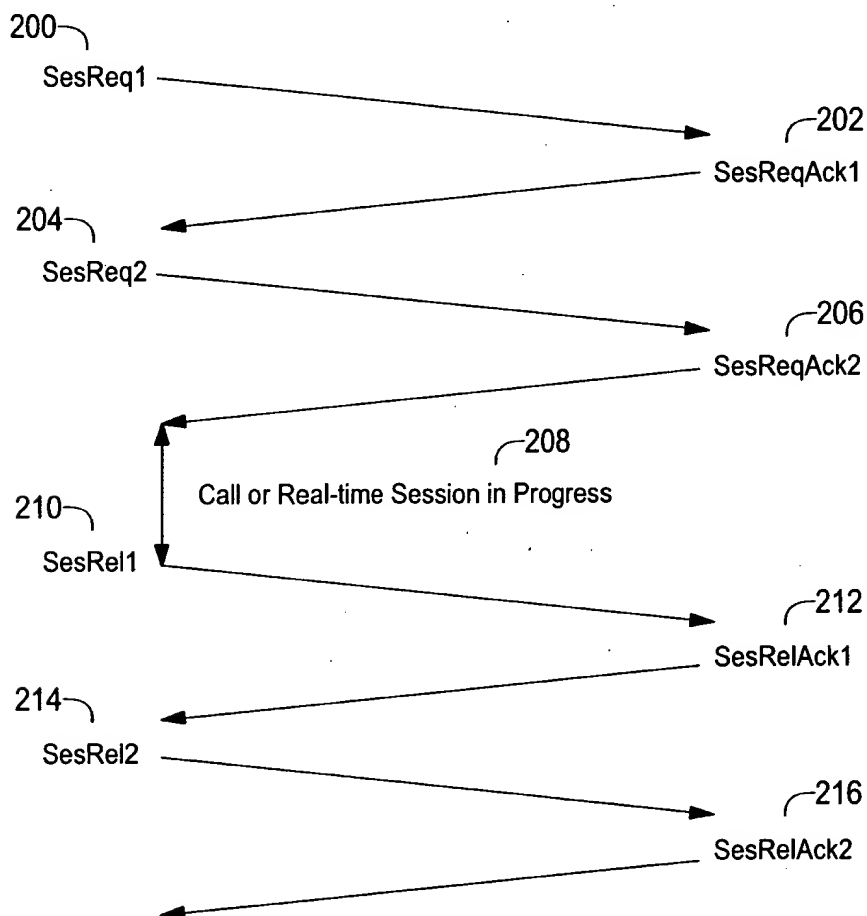


HEARTBEAT PACKET STRUCTURE

FIG. 5

Schedule
Server

SMC



Protocol Messages for a
Bi-directional Real-time Session
Establishment and Termination

FIG. 6

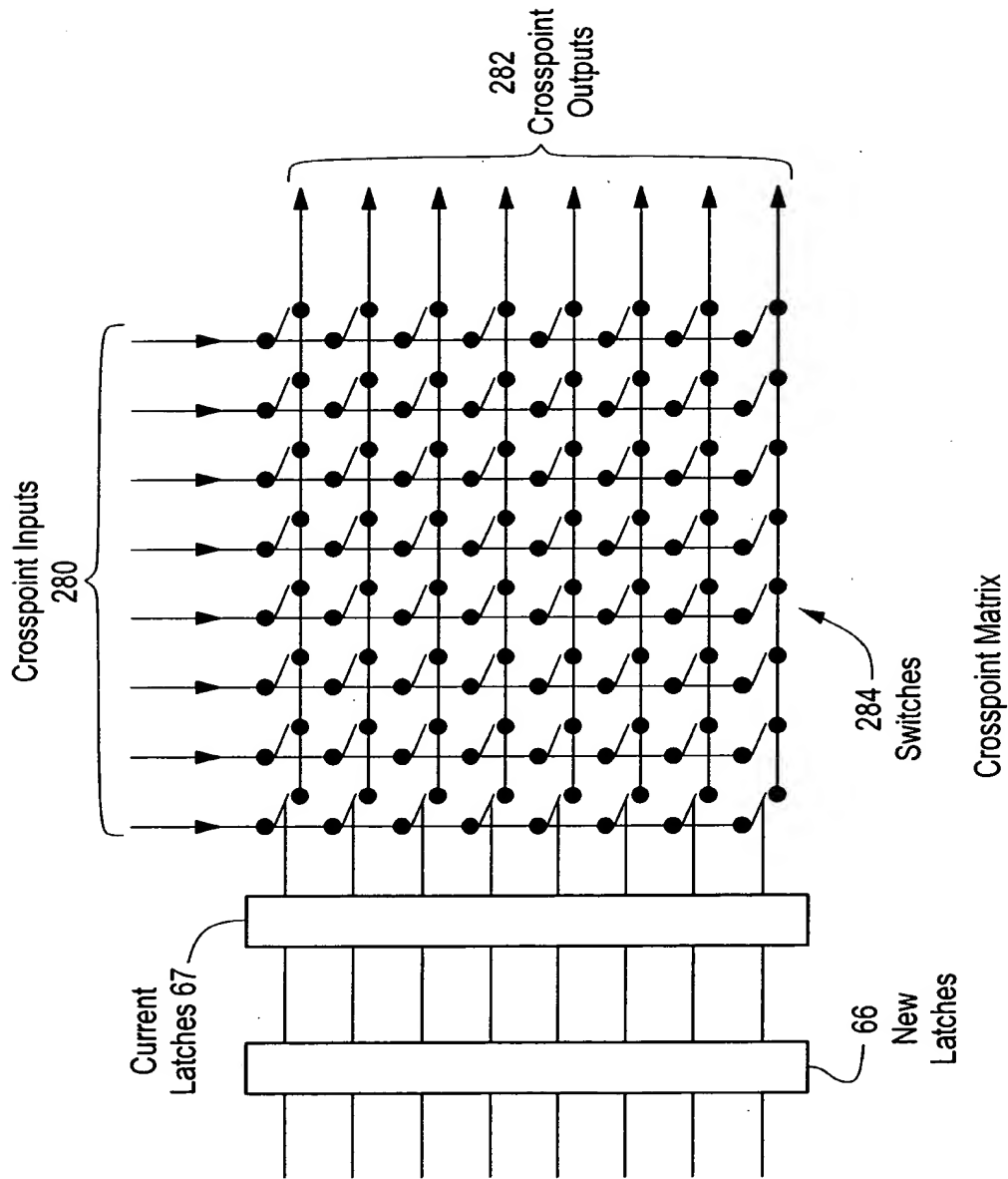
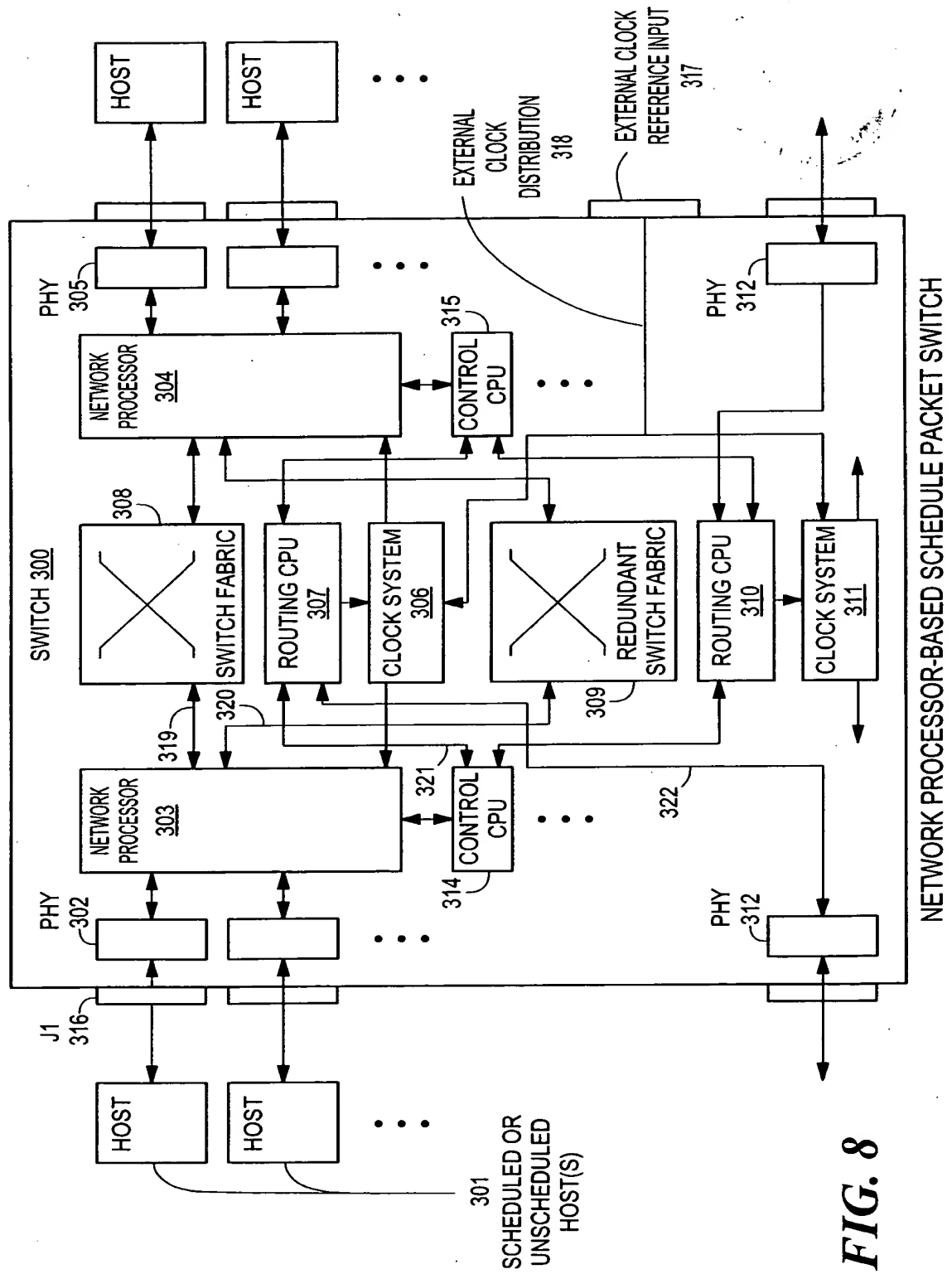
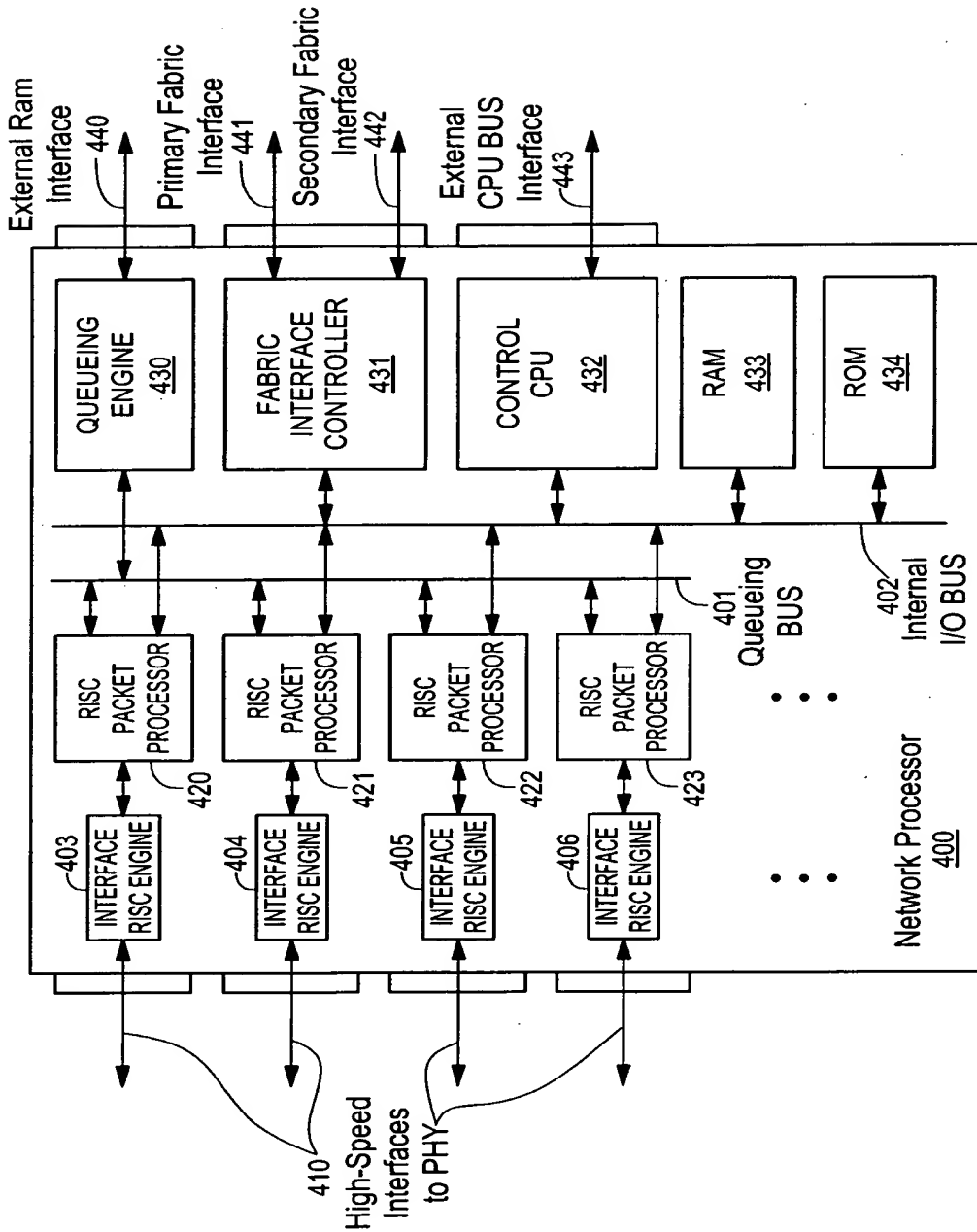


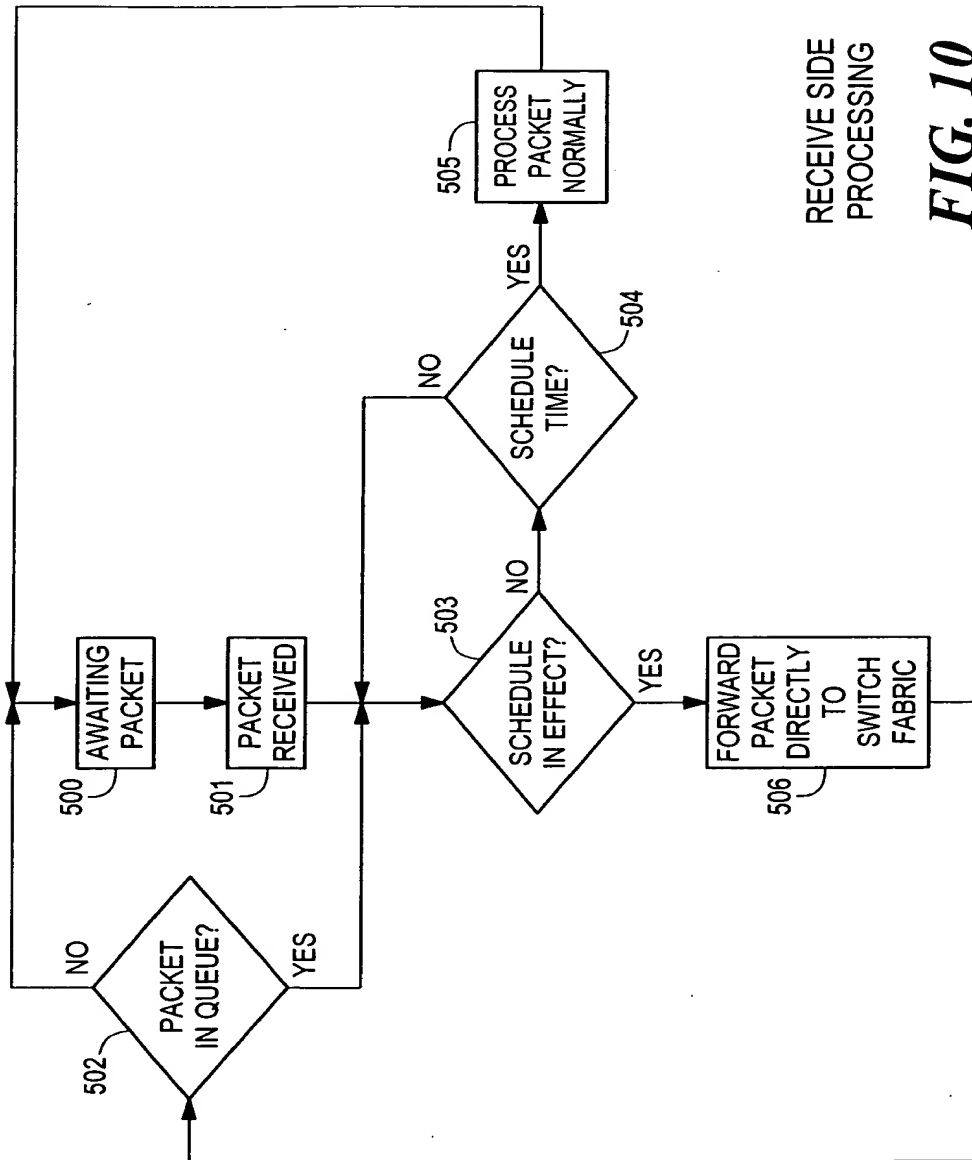
FIG. 7

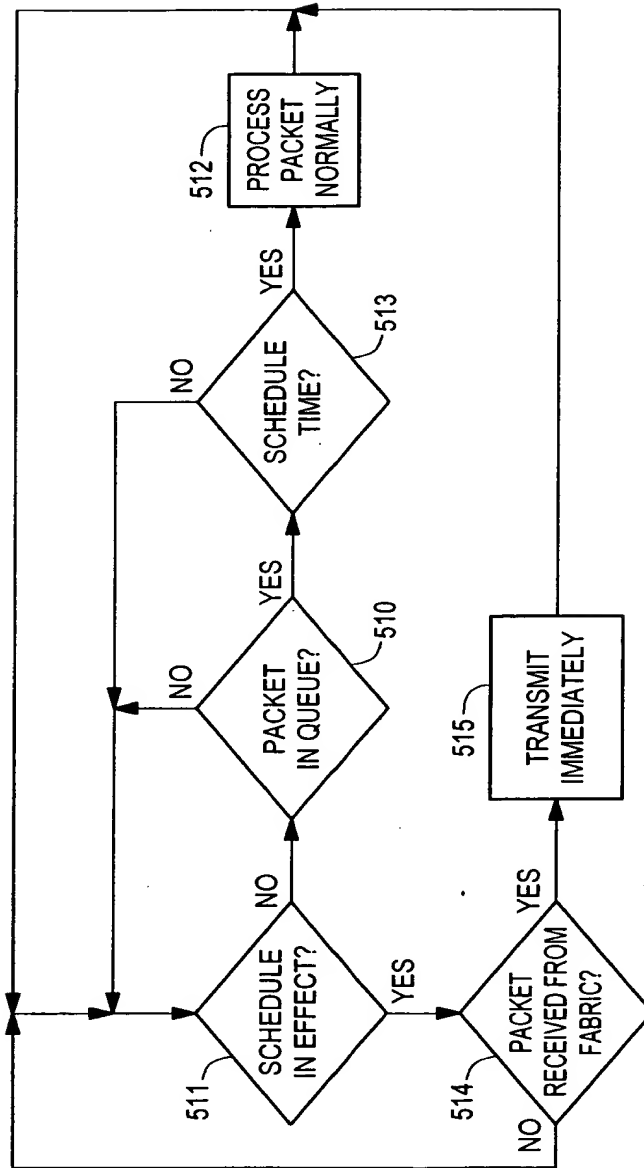




TYPICAL NETWORK PROCESSOR EMBODIMENT

FIG. 9

RECEIVE SIDE
PROCESSING**FIG. 10**



TRANSMIT SIDE
PROCESSING

FIG. 11